

IN THE SPECIFICATION:

Please replace paragraph [0033] with the following amended paragraph:

-- In FIG. 7, the shutdown ON and OFF circuit 126 may include a first resistor R11 and a second resistor R12 connected in series between a second input terminal 152 and a base terminal of a first transistor Q11, a third resistor R13 connected to an emitter terminal of the first transistor Q11 and a connection node between the first and second resistors R11 and R12, a fifth resistor R15 connected between a collector terminal of the first transistor Q11 and a base terminal of a second transistor Q12, a fourth resistor R14 connected between the collector terminal of the first transistor Q11 and an emitter terminal of the second transistor Q12 such that the emitter terminal of the second transistor Q12 is connected to the input voltage V_{in} , a sixth resistor R16 connected to a collector terminal of the second transistor Q12 and gate terminals of first and second field effect transistors Q13 and Q14, a seventh resistor ~~R7~~ R17 connected to the low path of the backlight lamp to be connected to a source terminal of the first field effect transistor Q13 such that a source terminal of the second field effect transistor Q14 is connected to the source terminal of the first field effect transistor Q13 and a drain terminal of the second field effect transistor Q14 is connected to a comparison terminal CMP of an error amplifier 129, a second capacitor C2 connected between ground GND and the drain terminal of the second field effect transistor Q14, a first capacitor C1 connected between a drain terminal of the first field effect transistor Q13 and the drain terminal of the second field effect transistor Q14, wherein the drain terminal of the first field effect transistor Q13 is connected to a feedback FB terminal of the error amplifier 129.--